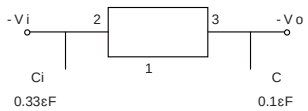
Parameter	Symbol	Value	Unit
Input Voltage	V_i		V
Thermal esistance unction	$R_{\theta JA}$		¡ã/W
Operating Junction Temperature Range	T_{OPR}		¡ã
Storage Temperature Range	T_{STG}	-65 150	¡ã

Parameter	Symbol	Test conditions		Min	Typ	Max	Unit
Output oltage	Vo		25¡ã		-8.0	-8.32	V
		-10.5V≤V$_I$≤-23V, Io=1mA~40mA	0-1 5¡ã	-7.6	-8.0	-8.4	V
		Io=1mA~70mA		-7.6	-8.0	-8.4	V
Load Regulation	φVo	Io=1mA~100mA	25¡ã		30	100	mV
		Io=1mA~40mA	25¡ã		15	50	mV
Line egulation	φVo	-10.5V≤V$_I$≤-23V	25¡ã		42	200	mV
		-11V≤V$_I$≤-23V	25¡ã		36	150	mV
Quiescent Current	Iq		25¡ã		4	6	mA
Quiescent Current Change	φIq	-11V≤V$_I$≤-23V	0-1 ¡ã			1.5	mA
	φIq	1mA≤I$_O$≤40mA	0-1 5¡ã			0.1	mA
Output Noise Voltage	V_N	10Hz≤f≤100KHz	25¡ã		54		V/Vo
Ripple Rejection	RR	-11V≤V$_I$≤-21V,f=120Hz	0-1 ¡ã	37	46		dB
Dropout Voltage	Vd		25¡ã		1.7		V

* Pulse test.

TYPICAL APPLICATION



Note: Bypass capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulators.